



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Haruo Hyodo et al.

Art Unit : 2822

Serial No. : 09/963,267

Examiner : Ida M. Soward

Filed : September 26, 2001

Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF OCTOBER 24, 2003

Please amend the above-identified application as follows:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 24, 2004

Date of Deposit

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